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Examiner Name Erin M. File
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Patent No. Patent No. Total Number of Pages in This Submission Attorney Docket Number X-1359 US
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Firm or Customer Number Attn: Attn: Justin Liu (Customer Number) Reg. Number 51,959
Signature
Date November 12, 2007 Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040
CERTIFICATE OF MAILING
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on this date:
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X-1359 US 10/660,254 PATENT

Conf. No.: 5349

IN THE UNITED STATES PATENT OFFICE

Applicants:

Eric D. Groen et al.

Assignee:

Xilinx, Inc.

Title:

MGT/FPGA CLOCK MANAGEMENT SYSTEM

Serial No.:

10/660,254

File Date:

9/11/2003

Examiner:

Sophia Vlahos

Art Unit:

2611

Docket No.:

X-1359 US

Conf. No.:

5349

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AMENDMENT IN RESPONSE TO THIRD OFFICE ACTION

Dear Examiner:

In response to the Third Office Action mailed from the Patent Office on August 24, 2007, please replace/substitute the following claims as indicated.

Amendment to the Claims begin on page 2 of this paper.

Remarks begin on page 8 of this paper.